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TEL: 7036414547

COMMENT:

PAGE 1/4 * RCVD AT 2/27/2004 11:52:41 AM [Eastern Standard Time] * SVR:USPTO-EFXXRF-2/0 * DNIS:7469195 * CSID:7036414357 * DURATION (mm-ss):01-00

ReedSmith**FAX TRANSMITTAL**

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Date: February 27, 2004

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Total Number of Pages Including Cover Page **3****TO**Office of Initial Patent Examination's Filing Receipt
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NOTES: Re: Serial #10/717,705 Filed: November 21, 2003 - It was just brought to our attention that the filing receipt regarding serial # 10/717,705 is incorrect as received from the USPTO. Please issue a correct filing receipt showing the proper spelling of the applicant's name as: Naoki YAMAMOTO (instead of Yamamot as currently shown on the attached filing receipt).

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10/717,705	11/21/2003	2812	770	HITA.0462	18	20	3

REED SMITH LLP
3110 Fairview Park Drive, Suite 1400
Falls Church, VA 22042

CONFIRMATION NO. 8848

FILING RECEIPT

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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) **YAMAMOTO**
Naoki Yamamoto, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-369422 12/20/2002

If Required, Foreign Filing License Granted: 02/24/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Fabrication process of a semiconductor integrated circuit device

Preliminary Class

438

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